

Docket No.: GR 98 P 4137 P

I hereby certify that this correspondence is being deposited with the United States Postal Service with sufficient postage as First Class Mail in an envelope addressed to the Assistant Commissioner for Patents, Washington, D.C. 20231, on the date indicated below.

By: H P

Date: October 14, 2002

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Applicant : Hans-Jürgen Hacke et al.
Applic. No. : 09/761,594
Filed : January 16, 2001
Title : Semiconductor Device in Chip Format and Method for Producing It
Examiner : John T. Haran Art Unit: 1733

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TC 1700

INFORMATION DISCLOSURE STATEMENT
UNDER 37 C.F.R. 1.97(D)

Hon. Commissioner of Patents and Trademarks,
Washington, D.C. 20231

Sir:

In accordance with 37 C.F.R. 1.98 copies of the following patents and/or publications
are submitted herewith:

United States Patent No. 5,340,775 (Carruthers et al.), dated August 23, 1994;

United States Patent No. 5,663,086 (Rostoker et al.), dated September 2, 1997;

United States Patent No. 5,672,542 (Schwiebert et al.), dated September 30, 1997;

John R. Morris et al.: "Stencil Printing of Solder Paste for Fine-Pitch Surface Mount Assembly", *IEEE Transactions on Components, Hybrids, and Manufacturing Technology*, Vol. 14, September 1991, No. 3, pp. 560-566;

P. Van Zant: "Microchip Fabrication" Chapter 7, *McGraw Hill*, New York, 1998, pp. 139-146, 598-599.

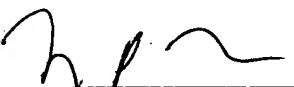
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In accordance with 37 C.F.R. 1.97(e) the undersigned herewith states that each item of information contained in the information disclosure statement was first cited in a communication from a foreign patent office in a counterpart foreign application not more than three months prior to the filing of the information disclosure statement.

In accordance with 37 C.F.R. 1.97 (d), consideration of this Information Disclosure Statement is requested.

Enclosed is the fee in the amount of \$180.00.

Respectfully submitted,



For Applicants

Mark P. Weichselbaum
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Date: October 14, 2002

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